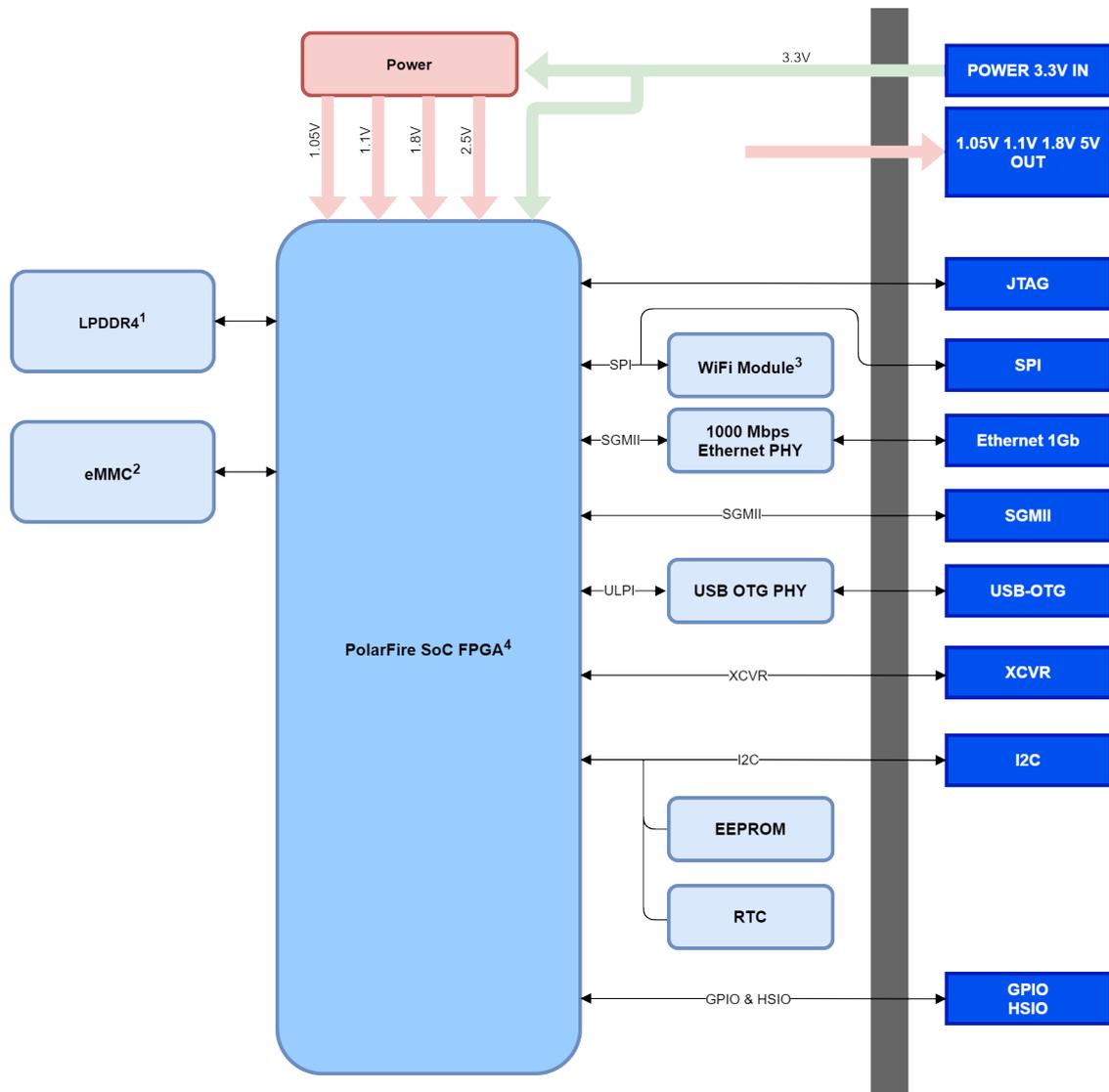# 5. Block diagram

Fig. 5.1 Conclusive Engineering RCHD-PF SoM block diagram.

[1] Size and variant (LPDDR4) of RAM memory is determined by the SOM version.

[2] Size of EMMC memory is determined by the SOM version.

[3] WiFi module can be present or absent, determined by the SOM version. If absent, the MSS SPI interface is available on the header.

[4] Part number of PolarFire SoC FPGA is determined by the SOM version.